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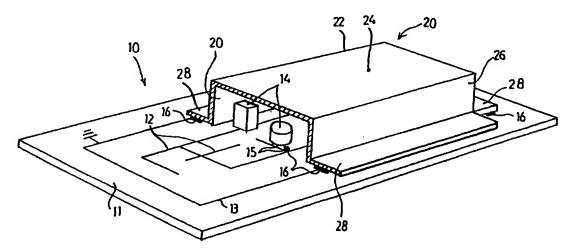
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(54) Title: METHODS OF MANUFACTURING A PRINTED CIRCUIT BOARD SHIELDED AGAINST INTERFERING RADIATION



(57) Abstract: Methods for the production of a board (11) with printed circuit (12) shielded against interfering radiation and having electronic components (14) comprise the steps of positioning of the electronic components (14) on contact points (15) designed for them, and the application of a shield (20), comprising a preformed metallized plastic film (30) over the top of the electronic components (14) and in electrical contact with the earth (13) on the board (11) with printed circuit (12), and also fixing the electronic components (14) on the board (11) with printed circuit (12) by means of an electrically conducting fixing agent (16); and fixing of the shield (20) on the board (11) with printed circuit (12).





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Methods of manufacturing a printed circuit board shielded against interfering radiation

The present invention relates to a method for the production of a board with printed circuit (printed circuit board) shielded against interfering radiation and having at least one electronic component.

It is customary for an electronic device to be protected, either in order to protect the electronic device itself against interfering radiation coming from an external source or in order to protect the environment against radiation generated by the electronic device itself.

10 Electronic devices which may be sensitive to interfering radiation, such as electromagnetic radiation, are electronic regulating, control and switching devices, and likewise communications equipment and data processing equipment, comprising one or more boards having a circuit on them and provided with electronic components, which circuit comprises a network of electrical conductors. Examples of such devices comprise, for example, microdata processors, computers, integrated circuits, microswitches, mobile telephones, transmitting and receiving equipment, pagers, television and so forth.

The term "shielding" is used in the art when referring to such protection of electronic equipment. The requirements which the abovementioned devices have to meet as regards such protection—are becoming increasingly strict, and to an increasing extent are being laid down at international level.

Suitable shielding materials are generally composed of materials with good electrical and/or magnetic conducting properties, and are consequently often composed of metal. Examples of such shielding are metal casings or plastic coverings provided with a thin metal layer and generally applied to the inside of a housing. Various techniques are known for the application of such a metal layer directly to the inside of a housing or to a thin plastic film. Examples are metal painting, electroless plating, electroplating, vapour deposition and sputtering. In addition to these often complex, and therefore expensive techniques, in-mould foiling is a known process, in which either a metallized plastic film is deformed in a

mould, for example by deep drawing, or a previously deformed metallized plastic film is placed in a mould, and in both cases a molten resin is subsequently poured into the mould, in order to place a carrier against the deformed metallized plastic film. Said carrier is often the housing or a part of the housing. This technique and the shielding materials used in it are described in, for example, W099/40770 of the applicant.

EP-A-0,806,892 also discloses a production method, in which a shield is used, which shield consists of a plastic substrate which is 10 first deformed and then metallized. Such a metallization of an already deformed plastic substrate is, however, difficult to carry out. The shield is fixed by means of conducting adhesive tape to an earth track of a board with printed circuit.

Although some of the abovementioned techniques provide good

15 shielding results, there is a need among manufacturers of the
abovementioned electronic equipment for simple shielding techniques,
in particular for techniques that can be used by the manufacturer
himself during the assembly of the electronic equipment.

The present invention provides such techniques.

According to a first aspect of the invention, to that end the method for the production of a board with printed circuit shielded against interfering radiation and having at least one electronic component comprises the steps of

- a) the application of flowable electrically conducting fixing
   25 agent to contact points for the at least one electronic component on
   the board with printed circuit and to the earth for the shield;
  - b) the positioning of the at least one electronic component on the contact points concerned of the board with printed circuit;
- the positioning of a shield, comprising a preformed
   metallized plastic film, over the top of the electronic component on the earth of the board with printed circuit; and
- d) the simultaneous fixing of the at least one electronic component on the contact points and the shield on the earth by increasing the temperature to above the flowing point of the flowable 35 electrically conducting fixing agent.

In the case of this method according to the invention, a flowable electrically conducting fixing agent, such as solder or conducting polymer, for example in solid form, is first applied to the contact points and to an earth track. For the shielding, the flowable fixing agent can advantageously be applied in the form of a

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so-called microball grid array. The contact points are in communication with the printed circuit (network of electrical conductors). The one or more electronic components are then placed, and the shield is subsequently placed over the top of the electronic components. The temperature is then increased to above the flow temperature of the fixing agent. In the case of soldering paste such a temperature increase can be carried out, for example, in an oven by heating to 260°C or more, depending on the type of soldering paste used. The preformed shield retains its shape here. When the fixing agent has flowed sufficiently, the entire unit is allowed to cool down, so that solidification of the fixing agent can take place and both the shield and the electronic components are thereby firmly fixed on the PCB. The result of this is that both the electronic components and the shield are fixed on the PCB in one step, instead of being fixed in successive individual steps.

In the case of this method the shield is composed of a plastic film, which is moulded to the desired shape after metallization.

After fixing of the shield, the latter is in electrical contact with a separate earth track of the PCB. This, as it were, forms a Faraday cage. These steps of this process are relatively simple and can easily be carried out by the manufacturer himself and integrated in existing production lines using already existing equipment. In addition, the shape of the shield can be kept relatively simple, and therefore so can the process for deformation of the metallized plastic film. This also gives an advantage compared with a shield comprising a preformed and subsequently metallized plastic substrate, such as, for example, according to EP-A-0,806,892. A simpler form of the ultimate shield also gives the advantage that the chance of interruptions or thickness differences to occur in the metal layer during the deformation is slight.

A relatively simple shield advantageously comprises a substantially box-shaped element (also known as a shielding cap), the side wall(s) of which is/are provided with a projecting edge extending parallel to the main surface of the PCB, preferably an outward extending side edge with a view to the prior deformation. The box-shaped element may be a round, rectangular or square container, or may have any other suitable appearance. Such a box-shaped element is easy to position on and fix to the PCB by means of existing devices, for example the so-called pick-and-place machines, robot-

the correct positions, and which are already in use for positioning electronic components in the correct position on the PCB.

Steps a) and b) are advantageously carried out by means of the already mentioned pick-and-place machines, in the case of this first variant preferably with the same pick-and-place machine. In this way, a very simple assembly is possible, in fact consisting of 2 phases, involving positioning, on the one hand, and fixing, on the other hand.

In the case of the method discussed above, the shield is also 10 exposed to a high temperature, so that the flowable, electrically conducting fixing agent begins to flow. The plastic of the metallized plastic film is therefore advantageously a temperature-resistant material (preferably > 260°C), such as polyimide, PEI, PEEK, PES, polyamide and the like, or a mixture of these, if desired in 15 combination with less temperature-resistant materials, such as PC. The deformation of the metallized plastic film in order to produce the shield in the desired shape from it is usually carried out at fairly high temperatures in the range between 100 and 300°C, in particular between 100 and 200°C. Thermoplastic plastics are 20 therefore particularly suitable. A preferred material for the plastic film is polyimide, on account of its good temperature resistance and deformation properties. Polyimide also has properties that are beneficial for the metallization itself. The application of the thin metal layer to the plastic substrate for producing the metallized 25 plastic film can be carried out by, for example, magnetron sputtering, thermal vacuum metallization, electroless plating and the like. Polyimide does not exhibit any evaporation phenomena of monomer material from the polymer during vacuum processes. In the case of sputtering the plastic is preferably non-halogen-containing, since in 30 a conventional sputter unit halogen-containing material generally behaves aggressively (corrosively). If the thin metal layer is deposited on the plastic substrate by means of plating methods, the plastic is advantageously resistant to chemicals, in particular to acids.

35 The metal of the metallized plastic film is advantageously selected from Sn, Ni, Cu, Pb, alloys and mixtures thereof. A preferred material comprises tin or a tin alloy, on account of the good deformation properties, as already recognized in the earlier mentioned publication WO-A-98/40770. The properties mentioned in that 40 publication, such as thicknesses of the plastic film and the layer of

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tin deposited on it, if desired with the interposition of a thin intermediate layer which promotes adhesion, or with a corrosion-resistant top layer, are likewise usable in the case of the present invention.

An example of a suitable shielding material is a thermally stable polyimide film deformable under vacuum, with a layer of tin 5 (+/- 0.5)  $\mu$ m thick, which has a resistance (as R per square) of 0.18 +/- 0.3 ohm/square.

According to a further aspect, the invention relates to a

10 method for the production of a board with printed circuit shielded
against interfering radiation and having at least one electronic
component, which method comprises the steps of

- a) the positioning and fixing of the at least one electronic component on contact points concerned of the board with printed
   15 circuit;
- b) the positioning of a shield, comprising a preformed metallized plastic film, over the top of the at least one electronic component on the earth of the board with printed circuit, the shield being a substantially box-shaped element, the side wall of which is provided with a fixing edge extending parallel to the main surface of the board, which fixing edge is provided with through apertures; and c) the fixing of the shield on the earth of the board with printed circuit, using a fixing agent.

In the case of this variant of the method of the invention, in 25 a first step the electronic components are placed on the appropriate contact points of the PCB and fixed, for example with solder or conducting polymer. The shield is then placed over the top of the components thus positioned and is in electrically conducting contact with an earth track present on the PCB. In the case of this method 30 the shield used is made of a plastic substrate that is first metallized and is then deformed to a shape desired for the shield. Such a shield has the advantages mentioned earlier. In the case of this variant of the method the shield is produced in the shape of a substantially box-shaped element. Such a box-shaped element comprises 35 a bottom, vertical side walls and an open top side. On the edges bounding the top side, the vertical side walls are provided with fixing edges generally extending perpendicularly to the side walls and facing outwards. In the fixed position, said fixing edges run parallel to the main surface of the PCB. Through apertures are 40 provided in said fixing edges, which apertures can act as parts for

fixing the shield on the earth of the PCB. In a simple embodiment, adhesive tape which may or may not be conducting is used. Such adhesive tape comprises a (plastic) carrier, which is provided with an adhesive layer on at least one side. The carrier can also comprise a plastic foam, such as polyurethane foam, in particular polyurethane foam based on polyether chains, on account of the compressibility and adaptability. The (single-sided) adhesive tape, which may or may not be conducting, is placed at least partially on the side of the fixing edge facing away from the PCB, and partially on the board with printed circuit by way of the apertures. A flowable fixing agent, such as solder and conducting polymer, can also be used in the apertures. Other examples of such a fixing agent comprise a mechanical fixing agent such as clips and the like, interacting with holes or other fixing facilities provided in the board with printed circuit. The use of adhesive tape permits simple assembly.

A thermoplastic plastic can also be provided in the through apertures, which thermoplastic plastic flows when there is a temperature increase and hardens when there is cooling, in order to produce a connection between shield and board. It is important in that case for direct electrical contact to exist between the metal of the shield and the metal on the board. Direct electrical contact gives better conductance, and thus better protection. It also gives great durability.

The pick-and-place machines mentioned earlier can be used for carrying out the positioning of the electronic components and the shield. With regard to the type of starting materials for the shield, namely the plastic and metal covering, and also the production techniques used, reference is made to the discussion of these given above. Temperature resistance of the shield plays a subordinate role 30 here.

In yet a further variant of the method for the production of a board with printed circuit shielded with a shield against interfering radiation and having at least one electronic component, said method comprises the steps of

- 35 a) the positioning and fixing of the at least one electronic component on contact points concerned of the board with printed circuit;
- b) the positioning of a shield, comprising a preformed metallized plastic film, over the at least one electronic component
   40 on the earth of the board with printed circuit; and

c) the fixing of the shield on the earth of the board with printed circuit, using a conducting, double-sided adhesive material, the adhesion strength of the conducting, double-sided adhesive material to the shield being greater than its adhesion strength to 5 the earth of the board with printed circuit.

In the case of this variant the fixing of the shield is carried out using a conducting double-sided adhesive material, after the electronic components have first been positioned on and fixed to, for example soldered on, the PCB. Since in the case of this variant the 10 shielding material itself is not exposed to the high soldering temperatures, the requirements to be laid down for the shielding material as regards temperature resistance are lower. For example, PC can be used. Of course, the plastic materials mentioned above in the case of the first variant can also be used. The metal of the 15 shielding material can be selected from the metals or their alloys already mentioned. In addition, the use of double-sided adhesive tape which is conducting has the advantage that disassembly, for example for repair or recycling work, is relatively easy to carry out. The adhesion strength of the adhesive material in strip form to the 20 shield is greater than its adhesion strength to the PCB, for example by applying a different adhesive layer on either side to a suitable strip-shaped substrate. The adjustment of the adhesion strength by means of an adhesion gradient in the thickness direction of the adhesive layer is likewise a possibility and is a procedure which is 25 known in the art concerned. The strip-shaped adhesive material is further advantageously applied to the fixing edges of the box-shaped element, prior to the positioning of the shield on the PCB, because this is simpler than applying the strip-shaped adhesive material to the PCB, which is more difficult to gain access to it after the 30 electronic components have already been fixed on it.

An example of a suitable conducting, strip-shaped double-sided adhesive material is an acrylic polymer containing a conducting metal, the conducting metal advantageously being selected from silver, nickel, copper, alloys and mixtures thereof; for example, with a thickness of approximately 0.05 mm, an elongation of approximately 21%, and a resistance of less than 0.02 ohm/inch.

The invention is explained below with reference to the appended drawing, in which:

Fig. 1 is a cross-sectional view of a first embodiment of a PCB 40 with a shield acting against interfering radiation;

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Fig. 2 is a cross section of a detail of a soldered connection between PCB and shield in Fig. 1;

Fig. 3 is a cross-sectional view of a second embodiment of a PCB with a shield acting against interfering radiation;

Fig. 4 is a cross section of a detail of the connection between 5 PCB and shield in Fig. 3;

Fig. 5 is a cross-sectional view of a third embodiment of a PCB with a shield acting against interfering radiation; and

Fig. 6 is a cross section of a detail of the connection between 10 PCB and shield in Fig. 3.

Fig. 1 shows a cross section of a first embodiment of a PCB shielded against interfering radiation, indicated in its entirety by reference numeral 10. Said PCB 10 comprises a rectangular carrier 11, on which a system of electrical conductors 12 is provided as the 15 printed circuit, which conductors are applied or printed in a manner that is usual in the art. A separate earth track 13 is further provided. Electronic components 14 are provided at predetermined positions, here also called contact points 15, which components are fixed by means of solder 16 and are thus in contact with the 20 conductors 12. A shield 20 is provided on the PCB 10, in the embodiment shown said shield comprising a box 22 with a bottom 24 and side walls 26. At the open top side of the box 22, the side walls 26 are provided with outward extending fixing edges 28. The box 22 is formed from a metallized plastic film 30 by deep drawing at raised 25 temperature, which film in the case illustrated comprises a metal layer 32 facing the PCB and a plastic substrate 34 (see Fig. 2), for example a tin-plated polyimide film. If desired, a plastic film metallized on both sides can be used. The metal layer 32 is connected in a conducting manner to the earth track 13 by means of solder 16.

The embodiment illustrated in Figs. 1 and 2 is made as follows. A small quantity of solid solder 16 is applied to the contact points 15 on the board 11 with network of conductors 12 and a separate earth track 13, and to various contact positions on the earth track 13 for the shield 20. The electrical components 14 are then positioned on 35 the contact points 15 - for example, projecting contacts of the components 14 in corresponding recesses of the contact points. The shield 20 is subsequently positioned over the top of the electrical components 14. Both positioning steps are carried out with the same pick-and-place machine. The assembly thus prepared is placed in an 40 oven and heated to above the flowing point of the solder 16. Both the

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components 14 and the shield are fixed firmly to the PCB 10 by allowing the solder to flow and subsequently cool.

Figure 2 shows in greater detail the soldered connection thus formed between the earth track 13 and the shield 20 using solder 16.

Figure 3 shows another embodiment of a PCB shielded against interfering radiation. In this figure components corresponding to components from Figs. 1 - 2 are indicated by the same reference numerals. This embodiment of PCB 10 comprises a rectangular carrier 11, on which a system of electrical conductors 12 is provided as the 10 printed circuit, which conductors are applied or printed in a manner usual in the art. A separate earth track 13 is further provided. Electronic components 14 are provided at predetermined positions, the contact points 15, which components are fixed by means of solder 16, and are thus in contact with the conductors 12. A shield 20 is 15 provided on the PCB 10, which shield in the embodiment shown comprises a box 22 with a bottom 24 and side walls 26. On the open top side of the box 22, the side walls 26 are provided with outward extending fixing edges 28. The box 22 is formed by deep drawing at raised temperature from a metallized plastic film 30, which in the 20 instance shown comprises a metal layer 32 facing the PCB and a plastic substrate 34 (see Fig. 4), for example a tin-plated polyimide film. If desired, a plastic film metallized on both sides can be used. A number of through apertures 29 are made in the fixing edge, and a single-sided adhesive tape strip 31 is provided on the fixing 25 edge 28, which adhesive tape is forced through the apertures 29 until it is in contact with the board 11. This means that the fixing requires little surface area of the carrier 11. The metal layer 32 in this way is in direct contact with the earth track 13, as can be seen in greater detail from the cross section according to Figure 4.

The embodiment illustrated in Figs. 3 and 4 is made as follows. The electrical components 14 are positioned on the contact points 15 on the board 11 with network of conductors 12 and a separate earth track 13 - for example, projecting contacts of the components 14 in corresponding recesses of the contact points - and soldered with 35 solder 16. The shield 20 is then positioned over the top of the electrical components 14, and fixed with the single-sided adhesive tape 31 through the apertures 29. If the shield 20 and adhesive tape 31 are resistant to high temperature, if desired the soldering step of the Components 14 can be carried out after positioning of both the 40 components 14 and the shield 14, for example by heating in an oven to

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above the flowing point of the solder 16 and allowing to cool down. In that case it is easy to carry out both positioning steps with the same pick-and-place machine.

Figure 4 shows in greater detail the electrical connection 5 between the earth track 13 and the shield 20 when the single-sided adhesive tape 31 is used.

Figure 5 shows yet another embodiment of a PCB 10 shielded against interfering radiation. In this figure parts corresponding to parts from Figures 1 - 2 are indicated by the same reference 10 numerals. This embodiment of PCB 10 comprises a rectangular carrier 11, on which a system of electrical conductors 12 is provided as the printed circuit, which conductors are placed or printed in a manner usual in the art. A separate earth track 13 is further provided. Electronic components 14 are provided at predetermined positions, the 15 contact points 15, which are fixed by means of solder 16 and are thus in contact with the conductors 12. A shield 20 is provided on the PCB 10, which shield in the embodiment illustrated comprises a box 22 with bottom 24 and side walls 26. On the open top side of box 22 the side walls 26 are provided with outward extending fixing edges 28. 20 The box 22 is formed from a metallized plastic film 30 by deep drawing at raised temperature, which film in the case illustrated comprises a metal layer 32 facing the PCB and a plastic substrate 34 (see also Fig. 7), for example a tin-plated polyimide film. If desired, a plastic film metallized on both sides can be used. Strips 25 of a conducting, double-sided adhesive tape 40 (see Fig. 6) are provided on the fixing edges 28 of the box 22. After cooling of the assembly of PCB 10 and components 14, the box 22 prepared in this way is positioned by means of a pick-and-place machine at its intended position and adhered.

The adhesive tape 40 is composed of a carrier layer 42 with a conducting adhesive layer 44, 46 respectively on either side, each layer having its own adhesion strength, as explained above. The adhesive tape can also be made of a conducting adhesive matrix containing metal particles, which in the thickness direction is 35 provided with an adhesion gradient, so that its two surfaces have a different adhesion on the earth track 13 and the carrier 11, and the shield 20 respectively.

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### CLAIMS

- 1. Method for the production of a board with printed circuit shielded with a shield against interfering radiation and having at least one electronic component, which method comprises the following steps:
- the application of flowable electrically conducting fixing agent (16) to contact points (15) for the at least one electronic component (14) on the board (11) with printed circuit (12) and to the earth (13) for the shield (20);
- 10 f) the positioning of the at least one electronic component (14) on the contact points (15) concerned of the board (11) with printed circuit (12);
  - g) the positioning of a shield (20), comprising a preformed metallized plastic film (30), over the top of the at least one electronic component (14) on the earth of the board (11) with printed circuit (12); and
  - h) the simultaneous fixing of the at least one electronic component (14) on the contact points (15) and the shield (20) on the earth by increasing the temperature to above the flowing point of the flowable electrically conducting fixing agent (16).
- 2. Method according to claim 1, characterized in that the shield (20) comprises a substantially box-shaped element (22), the side wall (26) of which is provided with a fixing edge (28) extending parallel 25 to the main surface of the board (11).
  - 3. Method according to one of the preceding claims, characterized in that the steps b) and c) are carried out with a so-called pick-and-place device.
- 4. Method according to claim 3, characterized in that the steps b)
  30 and c) are carried out with the same pick-and-place device.
- 5. Method according to one of the preceding claims, characterized in that the plastic (34) of the metallized plastic film (30) is selected from temperature-resistant materials, such as polyimide, PC, PEEK, PES, polyamide and the like or mixtures thereof, preferably a polyimide.
  - 6. Method according to one of the preceding claims, characterized in that the metal (32) of the metallized plastic film (30) is selected from Sn, Ni, Cu, Pb, alloys and mixtures thereof.

- 7. Method according to claim 6, characterized in that the metal (32) comprises tin, lead or an alloy thereof.
- 8. Method for the production of a board with printed circuit shielded against interfering radiation and having at least one 5 electronic component, which method comprises the following steps:
  - d) the positioning and fixing of the at least one electronic component (14) on contact points (15) concerned of the board (11) with printed circuit (12);
- e) the positioning of a shield (20), comprising a

  10 preformed metallized plastic film (30), over the top of
  the electronic component (14) on the earth (13) of the
  board (11) with printed circuit (12), the shield (20)
  being a substantially box-shaped element (22), the side
  wall (26) of which is provided with a fixing edge (28)
  extending parallel to the main surface of the board (11),
  which fixing edge (28) is provided with through apertures
  (29); and
  - f) the fixing of the shield (20) on the earth of the board (11) with printed circuit, using a fixing agent.
- 20 9. Method according to claim 8, characterized in that the fixing agent is single-sided adhesive tape (31).
  - 10. Method according to one of the preceding claims 8 or 9, characterized in that the steps a) and b) are carried out with a so-called pick-and-place device.
- 25 11. Method according to one of the preceding claims 8 10, characterized in that the plastic (34) of the metallized plastic film (30) is selected from temperature-resistant materials, such as polyimide, PC, PEEK, PES, polyamide and the like or mixtures thereof, preferably a polyimide.
- 30 12. Method according to one of the preceding claims 8 11, characterized in that the metal (32) of the metallized plastic film (30) is selected from Sn, Ni, Cu, Pb, alloys and mixtures thereof.
  - 13. Method according to claim 12, characterized in that the metal
  - (32) comprises tin, lead or an alloy thereof.

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- 35 14. Method for the production of a board with printed circuit shielded against interfering radiation and having at least one electronic component, which method comprises the following steps:
  - d) the positioning and fixing of the at least one electronic component (14) on contact points (15) concerned of the board (11) with printed circuit (12);

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e) the positioning of a shield (20), comprising a preformed metallized plastic film (30), over the top of the at least one electronic component (14) on the earth (13) of the board (11) with printed circuit (12), and

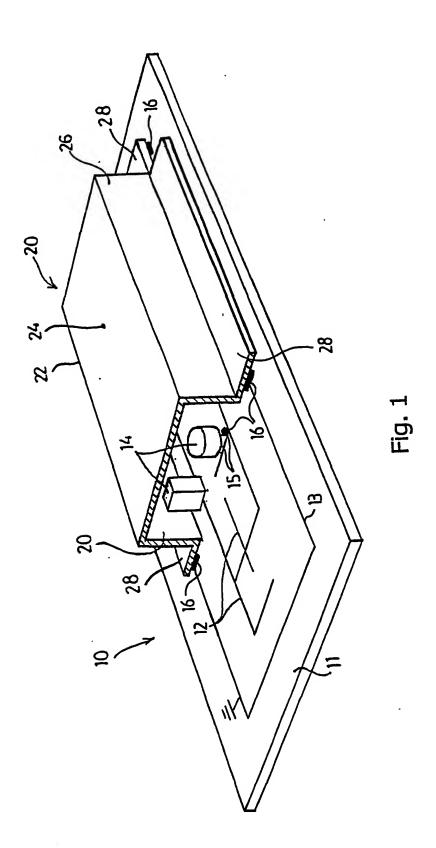
- f) the fixing of the shield (20) on the earth of the board (11) with printed circuit, using a conducting, double-sided adhesive material (40), the adhesion strength of the conducting, double-sided adhesive material (40) to the shield (20) being greater than its adhesion strength to the earth of the board (11) with printed circuit (12).
- 15. Method according to claim 14, characterized in that the conducting, strip-shaped, double-sided adhesive material (40) is a conducting metal-containing acrylic polymer.

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- 16. Method according to claim 15, characterized in that the
  15 conducting metal is selected from silver, nickel, copper, alloys and mixtures thereof.
  - 17. Method according to one of claims 14 16, characterized in that the steps a) and b) are carried out using different pick-and-place machines.
- 20 18. Method according to one of the preceding claims 14 17, characterized in that the plastic (34) of the metallized plastic film (30) is selected from temperature-resistant materials, such as polyimide, PC, PEEK, PES, polyamide and the like or mixtures thereof, preferably a polyimide.
- 25 19. Method according to one of the preceding claims 15 19, characterized in that the metal (32) of the metallized plastic film (30) is selected from Sn, Ni, Cu, Pb, alloys and mixtures thereof.

  20. Method according to claim 19, characterized in that the metal (32) comprises tin, lead or an alloy thereof.
- 30 21. Method according to one of the preceding claims 14 20, characterized in that the shield (20) is a substantially box-shaped element (22), the side wall (26) of which is provided with a fixing edge (28) extending parallel to the main surface of the board (11).



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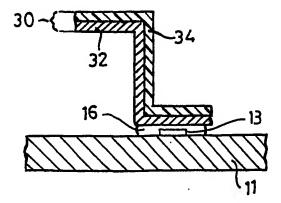


Fig. 2

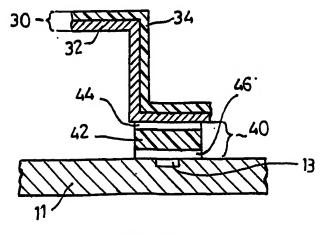
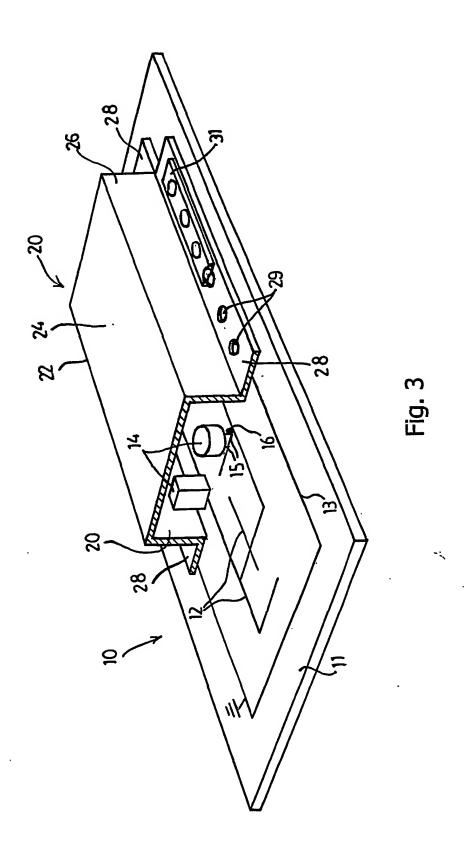
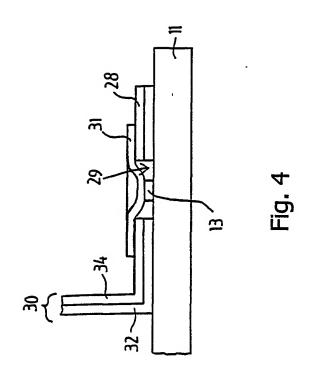
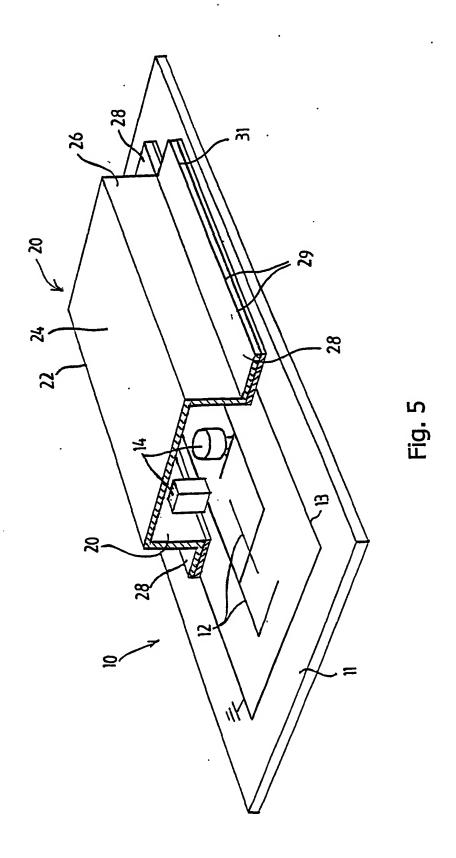


Fig. 6





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A. CLASSIFICATION OF SUBJECT MATTER IPC 7 H05K9/00

According to International Patent Classification (IPC) or to both national classification and IPC

### B. FIELDS SEARCHED

 $\begin{array}{ll} \mbox{Minimum documentation searched} & \mbox{(classification system followed by classification symbols)} \\ \mbox{IPC 7} & \mbox{H05K} \end{array}$ 

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

### EPO-Internal

C. DOCUM	ENTS CONSIDERED TO BE RELEVANT	
Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 0 806 891 A (GORE & ASS) 12 November 1997 (1997-11-12) column 3, line 37 - line 50; claim 1	1
A	EP 0 806 892 A (GORE & ASS) 12 November 1997 (1997-11-12) claims 1-4	1
A	US 5 313 371 A (KNECHT THOMAS A ET AL) 17 May 1994 (1994-05-17)	1
Α	column 5, line 12 —column 21; claim 1	Ì
Α	WO 99 40770 A (MACHIELSE JACOB JOOST; BERGSHOEFF PIETER DICK (NL); BLANKENBORG ST) 12 August 1999 (1999-08-12) cited in the application the whole document	
	-/	

Further documents are listed in the continuation of box C.	Patent family members are listed in annex.		
Special categories of cited documents:      A* document defining the general state of the art which is not considered to be of particular relevance     E* earlier document but published on or after the international filing date      L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)      O* document referring to an oral disclosure, use, exhibition or other means      P* document published prior to the international filing date but later than the priority date claimed	"T" later document published after the International filing date or priority date and not in conflict with the application but clied to understand the principle or theory underlying the invention  "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone  "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.  "&" document member of the same patent family		
Date of the actual completion of the international search	Date of mailing of the International search report 04/02/2002		
28 January 2002			
Name and mailing address of the ISA  European Patent Office, P.B. 5818 Patentlaan 2  NL – 2280 HV Rijswijk  Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,  Fax: (+31-70) 340-3016	Authorized officer  Toussaint, F		

INTERNATIONAL SEARCH REPORT

PCT7NL 01/00727

		TOTAL OI	PCT/NL 01/00/2/		
C.(Continua	ation) DOCUMENTS CONSIDERED TO BE RELEVANT				
Category °	Citation of document, with indication, where appropriate, of the relevant passages		Relevant to claim No.		
A	WO 00 48441 A (ERICSSON INC) 17 August 2000 (2000-08-17) page 10, line 28 -page 11, line 22		1		
	WO 98 47340 A (TELEFONAKTIEBOLAGET LM ERCISSO) 22 October 1998 (1998-10-22) the whole document		1		

Patent document		Publication date		Patent family member(s)	Publication date
cited in search report		]	110		02-06-1998
EP 0806891	Α	12-11-1997	US	5761053 A 5763824 A	02-00-1998
			US DE	69700126 D1	15-04-1999
			DE	69700126 D1 69700126 T2	19-08-1999
			DE	69700126 12 69700261 D1	15-07-1999
			DE	69700261 T2	30-12-1999
			EP	0806891 A1	12-11-1997
			EP	0806892 A1	12-11-1997
			ES	2132997 T3	16-08-1999
			JP	10070387 A	10-03-1998
			JP	10056285 A	24-02-1998
	 А	12-11-1997	us	5761053 A	02-06-1998
EP 0806892	٨	12-11-1997	DE	69700126 D1	15-04-1999
			DE	69700126 T2	19-08-1999
			DE	69700261 D1	15-07-1999
•			DE	69700261 T2	30-12-1999
			EP	0806891 A1	12-11-1997
			EP	0806891 A1	12-11-1997
			ES	2132997 T3	16-08-1999
			JP	10070387 A	10-03-1998
			JP	10070387 A 10056285 A	24-02-1998
			US	5763824 A	09-06-1998
					00.00.1004
US 5313371	Α	17-05-1994	JP `	6505597 T	23-06-1994
			MO	9216095 A1	17-09-1992
WO 9940770	Α	12-08-1999	NL	1008197 C2	05-08-1999
			ΑU	2302399 A	23-08-1999
			BR	9909093 A	05-12-2000
			CN	1290473 T	04-04-2001
			EP	1051891 A1	15-11-2000
			HU	0102091 A2	28-09-2001
			WO	9940770 A1	12-08-1999
			NO	20003942 A	04-10-2000
			PL	342037 A1	21-05-2001
			TR	200002277 T2	21-11-2000
	•		ZA	9900881 A	05-08-1999
WO 0048441	A	17-08-2000	US	6175077 B1	16-01-2001
			AU	2608600 A	29-08-2000
			WO	0048441 A1	17-08-2000
WO 9847340	A	22-10-1998	SE	511926 C2	20-12-1999
5047.010	• •		ĂŪ	734134 B2	07-06-2001
			AU	7092098 A	11-11-1998
			BR	9808555 A	23-05-2000
			CN	1252923 T	10-05-2000
			EE	9900454 A	17-04-2000
	•		EP	0976311 A1	02-02-2000
					30-10-2001
			J۲	2001520802 T	20 10 5001
			JP SE		17-10-1998
			SE	9701417 A	
					17-10-1998